AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions, and listings, of claims in this application.

Listing of Claims:

1. (Original) An image processing system comprised of an image reading device for optically reading original documents and converting these into electronic image data, and a printing device for printing image data that have been received from the image reading device onto recording media, the image processing system further comprising:

a first image processing unit that is provided on the image reading device side for processing read image data with at least a part thereof being comprised by a device which circuit logic is variable,

a second image processing unit that is provided on the printing device side for processing image data received from the image reading device,

a recording medium for storing a plurality of circuit arrangement information, and

a control unit for discriminating specifications for processing of the second image processing unit, selecting one of the plurality of circuit arrangement information in accordance with the discriminated specifications for processing, and setting a circuit logic for the first image processing unit based on the selected circuit arrangement information.

2. (Original) The image processing system as claimed in Claim 1, wherein the specifications for processing that are discriminated by the control unit are specifications for processing related to at least one of resolution, colored/monochrome and number of halftones.

3. (Original) The image processing system as claimed in Claim 1, wherein the recording medium is provided on the printing device side and in which the control unit acquires circuit arrangement information that is stored in the recording medium upon communication with the printing device.

4. (Original) The image processing system as claimed in Claim 1, wherein the image processing system is further comprised with another image reading device that may communicate with the above-described image reading device and in which the control unit acquires circuit arrangement information that is stored in the recording medium upon communication with the other image reading device.

5. (Original) The image processing system as claimed in Claim 1, wherein the image processing system is further comprised with a non-volatile memory provided on the image reading device side, and in which the control unit performs control such that circuit arrangement information acquired from the recording medium are stored in the non-volatile memory and that these circuit information are read from the non-volatile memory when necessary for setting the circuit logic for the first image processing unit.

6. (Original) The image processing system as claimed in Claim 5, wherein the control unit reads circuit information from the non-volatile memory when switching the power source of the device ON for setting the circuit logic for the first image processing unit.

7. (Original) The image processing system as claimed in Claim 1, wherein the control unit compares the newly acquired circuit arrangement information with circuit arrangement information that is in compliance with the currently set circuit logic for the first image processing unit and inhibits setting of a circuit logic for the first image processing unit based on the newly acquired circuit arrangement information in case both information are identical.

8. (Original) The image processing system as claimed in Claim 1, wherein the control unit sets the circuit logic for the first image processing unit to be a circuit logic that is based on default specifications in case a specified condition is met.

9. (Original) The image processing system as claimed in Claim 8, wherein the control unit sets the circuit logic for the first image processing unit to be a circuit logic that is based on default specifications upon completion of a single job in case the specified condition is completion of a single job in the image processing system.

10. (Original) The image processing system as claimed in Claim 8, wherein the control unit sets the circuit logic for the first image processing unit to be a circuit logic that is based on default specifications when a standby time of the image

processing system has exceeded a specified time in case the specified condition is the standby time of the image processing system.

11. (Original) The image processing system as claimed in Claim 8, wherein the default specifications are specifications for processing that are most frequently used.

12. (Original) An image processing system comprised of an image reading device for optically reading original documents and converting these into electronic image data, and a printing device for printing image data that have been received from the image reading device onto recording media, the image processing system further comprising:

a first image processing unit that is provided on the image reading device side for processing read image data,

a second image processing unit that is provided on the printing device side for processing image data received from the image reading device with at least a part thereof being comprised by a device which circuit logic is variable,

a recording medium for storing a plurality of circuit arrangement information, and

a control unit for discriminating specifications for processing of the first image processing unit, selecting one of the plurality of circuit arrangement information in accordance with the discriminated specifications for processing, and setting a circuit logic for the second image processing unit based on the selected circuit arrangement information.

13. (Original) The image processing system as claimed in Claim 12, wherein the specifications for processing that are discriminated by the control unit are specifications for processing related to at least one of resolution, colored/monochrome and number of halftones.

14. (Original) The image processing system as claimed in Claim 12, wherein the recording medium is provided on the printing device side and in which the control unit acquires circuit arrangement information that is stored in the recording medium upon communication with the printing device.

15. (Original) The image processing system as claimed in Claim 12, wherein the image processing system is further comprised with another image reading device that may communicate with the above-described image reading device and in which the control unit acquires circuit arrangement information that is stored in the recording medium upon communication with the other image reading device.

16. (Currently Amended) The image processing system as claimed in Claim 12, wherein the image processing system is further comprised with a non-volatile memory provided on the ~~image reading device~~ printing device side, and in which the control unit performs control such that circuit arrangement information acquired from the recording medium are stored in the non-volatile memory and that these circuit information are read from the non-volatile memory when necessary for setting the circuit logic for the second image processing unit.

17. (Original) The image processing system as claimed in Claim 16, wherein the control unit reads circuit information from the non-volatile memory when switching the power source of the device ON for setting the circuit logic for the second image processing unit.

18. (Original) The image processing system as claimed in Claim 12, wherein the control unit compares the newly acquired circuit arrangement information with circuit arrangement information that is in compliance with the currently set circuit logic for the second image processing unit and inhibits setting of a circuit logic for the second image processing unit based on the newly acquired circuit arrangement information in case both information are identical.

19. (Original) The image processing system as claimed in Claim 12, wherein the control unit sets the circuit logic for the second image processing unit to be a circuit logic that is based on default specifications in case a specified condition is met.

20. (Original) The image processing system as claimed in Claim 19, wherein the control unit sets the circuit logic for the second image processing unit to be a circuit logic that is based on default specifications upon completion of a single job in case the specified condition is completion of a single job in the image processing system.

21. (Original) The image processing system as claimed in Claim 19, wherein the control unit sets the circuit logic for the second image processing unit to be a circuit logic that is based on default specifications when a standby time of the image processing system has exceeded a specified time in case the specified condition is the standby time of the image processing system.

22. (Original) The image processing system as claimed in Claim 19, wherein the default specifications are specifications for processing that are most frequently used.

23. (New) An image processing system for generating duplicate copies, comprising an image reading device for optically reading original documents and converting these into electronic image data, and a printing device for printing image data that have been received from the image reading device onto recording media, said image reading device and said printing device individually having an image processing circuit for processing image data according to a specified specification,

wherein, on at least one of both devices, said image processing circuit can be set for said specification based on a given circuit arrangement information, the image processing circuit being set according to a specification information for the image processing circuit on the other device.

24. (New) The image processing system as claimed in Claim 23, wherein a circuit for determining at least one of resolution, colored/monochrome and number of halftones based on said given circuit arrangement information can be modified in

said image processing circuit which can be set for said specification for processing said image data.

25. (New) The image processing system as claimed in Claim 23, wherein an arrangement information file for storing a plurality of circuit arrangement information given to said image processing circuit that can be set for said specification for processing image data, is provided on at least one of said image reading device and printing device.

26. (New) The image processing system as claimed in Claim 23, wherein a plurality of said image reading device and said printing device are provided and said arrangement information file is provided in at least one of the devices, and the arrangement information stored in said arrangement information file can be transferred via specified communication line to each device comprising said image processing circuit which can be set for said specification for processing image data.

27. (New) The image processing system as claimed in Claim 24, wherein an arrangement information file for storing a plurality of circuit arrangement information given to said image processing circuit that can be set for said specification for processing image data, is provided on at least one of said image reading device and printing device.

28. (New) The image processing system as claimed in Claim 24, wherein a plurality of said image reading device and said printing device are provided and said

arrangement information file is provided in at least one of the devices, and the

arrangement information stored in said arrangement information file can be

transferred via specified communication line to each device comprising said image

processing circuit which can be set for said specification for processing image data.

29. (New) The image processing system as claimed in Claim 25, wherein a

plurality of said image reading device and said printing device are provided and said

arrangement information file is provided in at least one of the devices, and the

arrangement information stored in said arrangement information file can be

transferred via specified communication line to each device comprising said image

processing circuit which can be set for said specification for processing image data.